Lab 2 Prep

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1. Code is available and and working in Keil.

2a. The DCW instructions store the addresses for the pins that are accessed, and addresses for other functions called.

2b. 150ns.

2c. 0xE608, the value of PF1 which is probably 0.

2d. Load PF1 into R1, so R0 doesn’t have to be reloaded each iteration.

2e. No the main loop and the ISR write to different pins on port F, so there is no critical section or race condition.

5. Jitter = 0

6. 